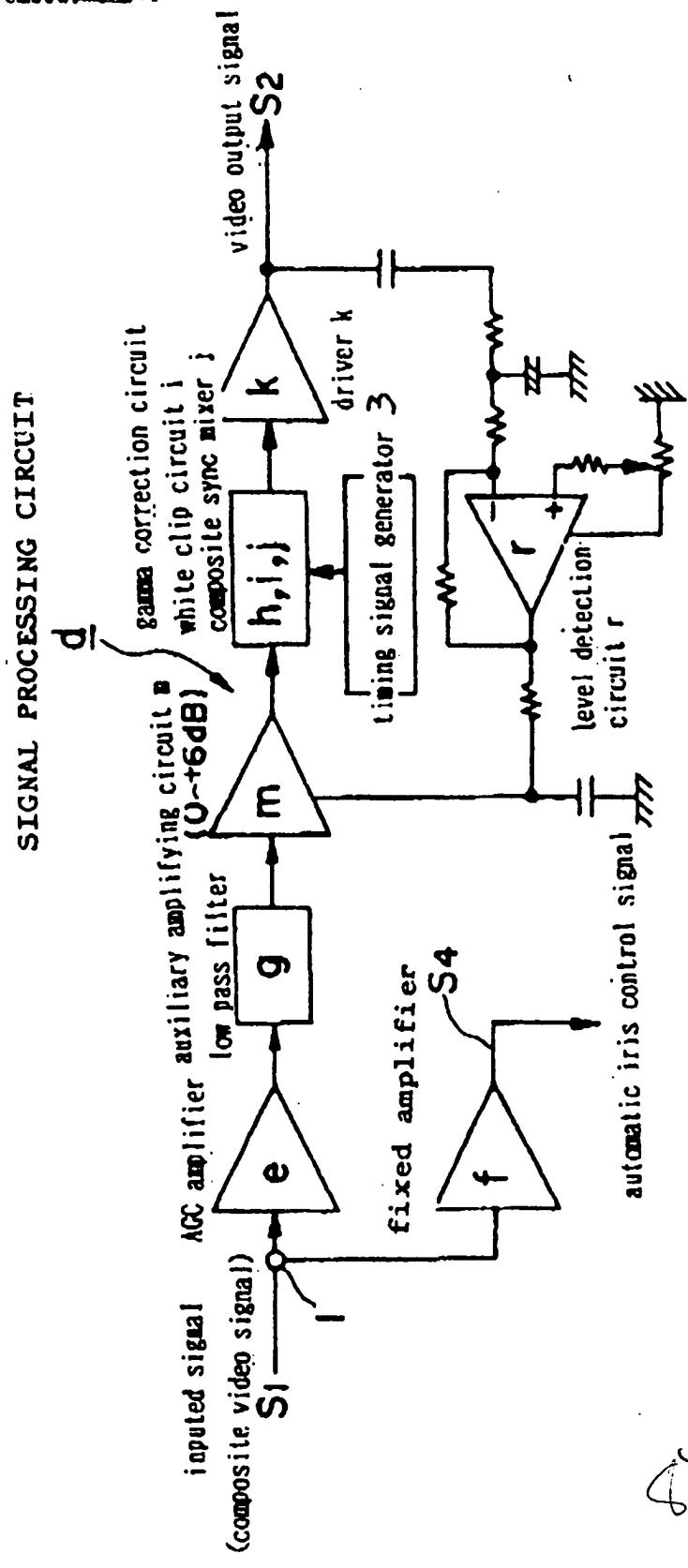


Fig 1 embodiment 1



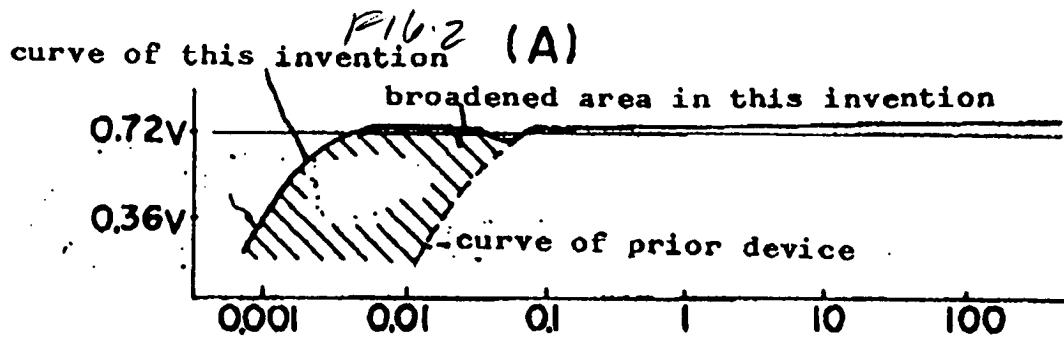


Fig 2 (A) object Lux (abscissa) and video output voltage (ordinate) of prior device (dotted line)

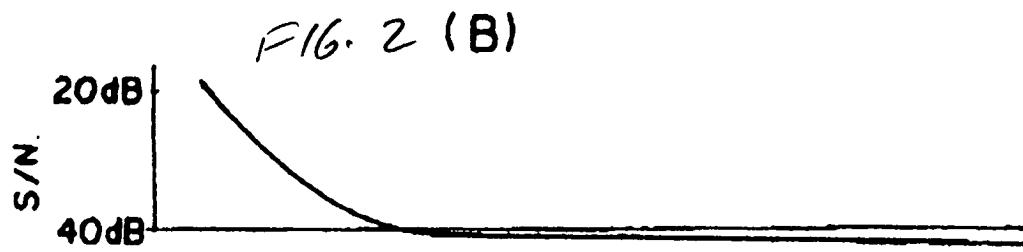


Fig 2 (B) object Lux (abscissa) and S/N ratio (ordinate) of this invention.

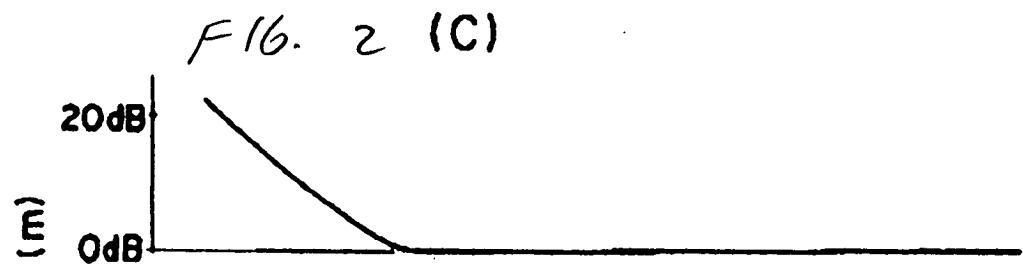


Fig 2(C) the relation of object Lux (abscissa) and gain of auxiliary amplifying circuit m (ordinate) of this invention.

Fig. 3. Embedded 2-

### Signal processing circuit

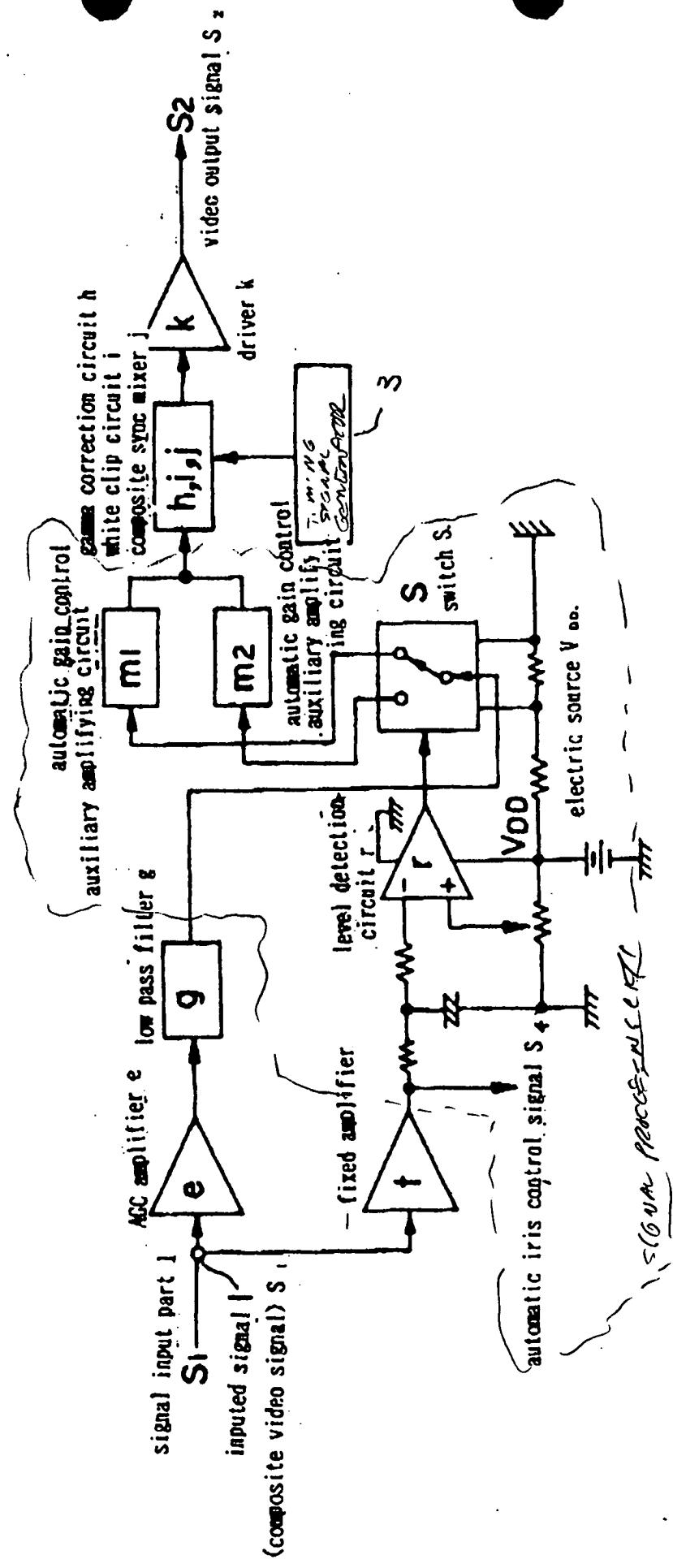


Fig 4. embodiment 3

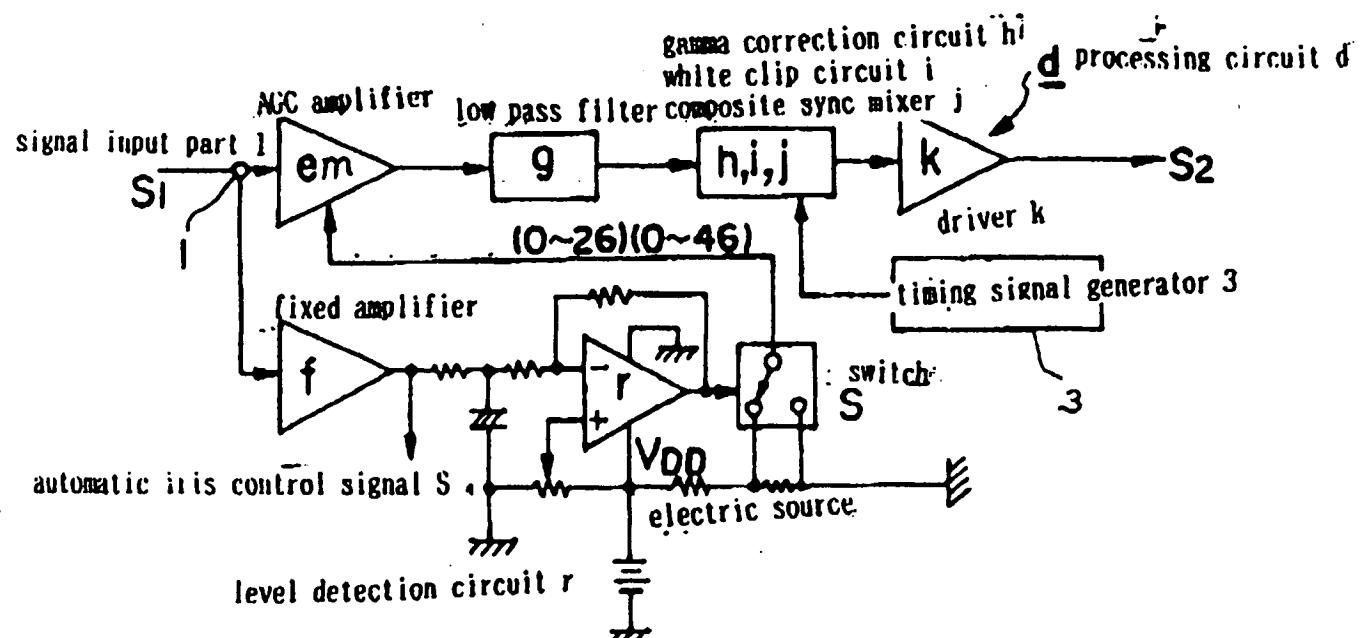


Fig 5

automatic iris control signal

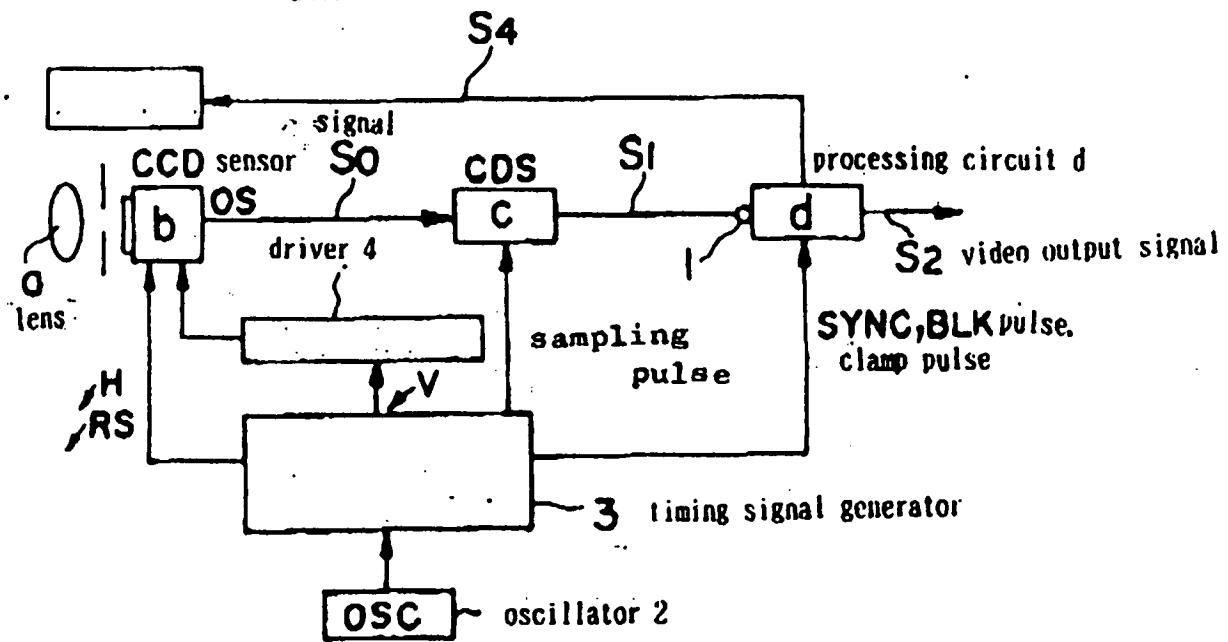
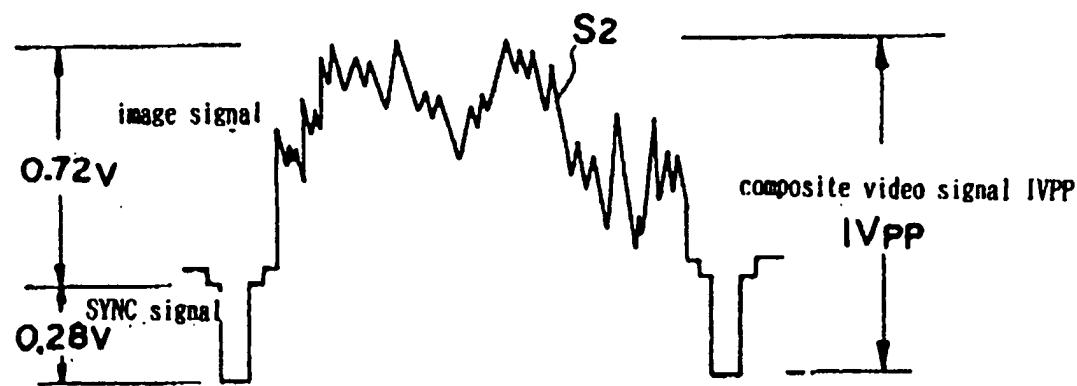


FIG. 6



(PRIOR ART)  
Fig 7 conventional device

